

SUPER[®]

SUPER[®]X6DH8-XG2
SUPER[®]X6DHE-XG2

USER'S MANUAL

Revision 1.1a

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Revision Number: Rev. 1.1a

Release Date: June 8, 2007

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Preface

About This Manual

This manual is written for system integrators, PC technicians and knowledgeable PC users. It provides information for the installation and use of the **SUPER**® X6DH8-XG2/X6DHE-XG2 motherboard. The **SUPER**® X6DH8-XG2/X6DHE-XG2 supports single or dual Intel® Nocona™ processors at a 800 MHz front side bus. Based upon Intel's NetBurst microarchitecture, the Nocona processor supports the IA-32 software and includes features found in the Xeon™ processor such as Hyper Pipelined Technology, which includes a multi-stage pipeline, allowing the processor to operate at much higher core frequencies. Packaged in a 604-pin Flip Chip Micro Pin Grid Array(FC-mPGA4) platform in a Zero Insertion Force(ZIF) socket (mPGA 604), the Nocona Processor (800 MHz) which supports Hyper-Threading Technology and Intel EM64T, is ideal for high performance workstation and server environments with up to two processors on one system bus. Please refer to the motherboard specifications pages on our web site (<http://www.supermicro.com/Products/>) for updates on supported processors. This product is intended to be professionally installed.

Manual Organization

Chapter 1 begins with a checklist of what should be included in your mainboard box, describes the features, specifications and performance of the motherboard and provides detailed information about the chipset.

Chapter 2 begins with instructions on handling static-sensitive devices. Read this chapter when you want to install the processor and DIMM memory modules and when mounting the mainboard in the chassis. Also refer to this chapter to connect the floppy and hard disk drives, SCSI drives, the IDE interfaces, the parallel and serial ports, the keyboard and mouse, the power supply and various control panel buttons and indicators.

If you encounter any problems, see **Chapter 3**, which describes troubleshooting procedures for the video, the memory and the setup configuration stored in CMOS. For quick reference, a general FAQ [Frequently Asked Questions] section is provided.

Chapter 4 includes an introduction to BIOS and provides detailed information on running the CMOS Setup utility.

Appendix A provides BIOS POST Messages.

Appendix B provides BIOS POST codes.

Appendix C provides software and the OS installation instructions.

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Chapter 1

Introduction

1-1 Overview

Checklist

Congratulations on purchasing your computer motherboard from an acknowledged leader in the industry. Supermicro boards are designed with the utmost attention to detail to provide you with the highest standards in quality and performance. Check that the following items have all been included with your motherboard. If anything listed here is damaged or missing, contact your retailer. All are included in the Retail Box.

One (1) Supermicro Mainboard

One (1) ribbon cable for IDE devices

One (1) floppy ribbon cable

One (1) Ultra 320 SCSI cable (*X6DH8-XG2 only)

One (1) COM2 cable

One (1) SATA cables

One (1) I/O backpanel shield

One (1) USB 2 Port cable

One (1) Supermicro CD or diskettes containing drivers and utilities

One (1) User's/BIOS Manual

One (1) Ultra 320 SCSI User's Manual (*X6DH8-XG2 only)

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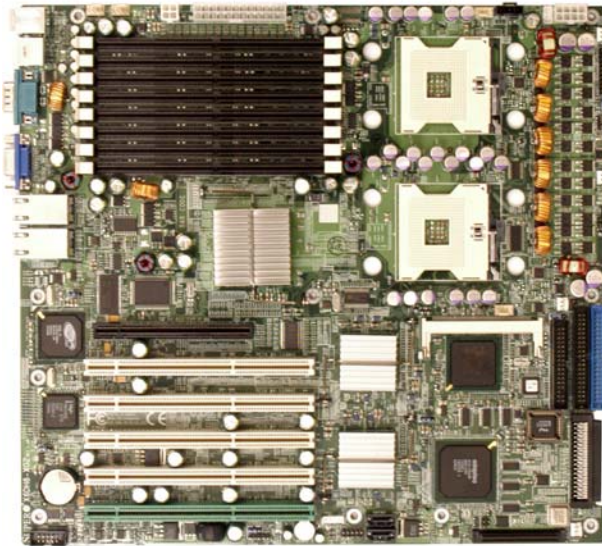
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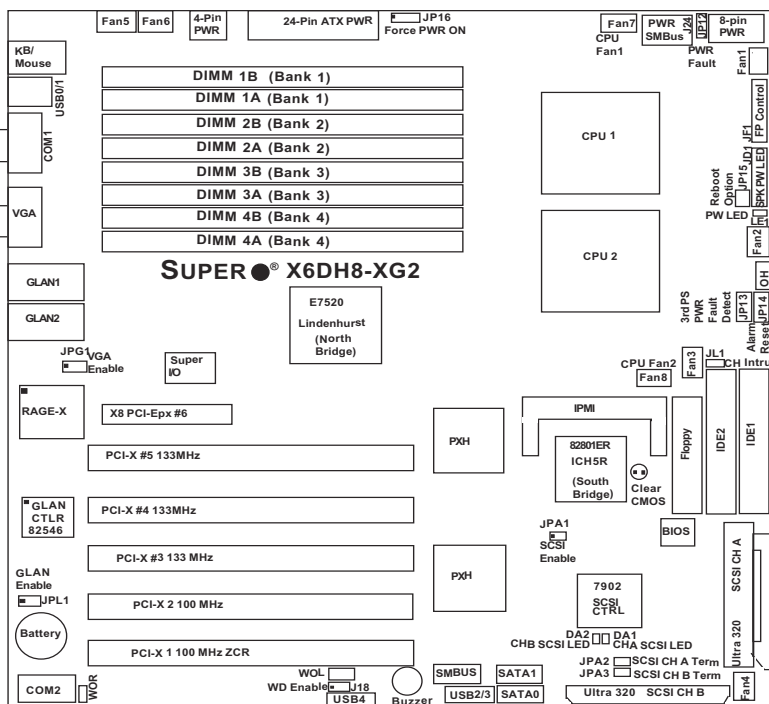
Figure 1-1. **SUPER** X6DH8-XG2/X6DHE-XG2 Image



(*Note: The drawings and pictures shown in this manual were based on the latest PCB Revision available at the time of publishing of this manual. The motherboard you've received may or may not look exactly the same as the graphics shown in the manual.)

Figure 1-2. **SUPER® X6DH8-XG2/X6DHE-XG2 Motherboard Layout**

(not drawn to scale)



Notes

1. Jumpers not indicated are for test purposes only.
2. See Chapter 2 for detailed information on jumpers, I/O ports and JF1 front panel connections.
3. "■" indicates the location of Pin 1.
4. SCSI is for the X6DH8-XG2 only
5. The drawings and pictures shown in this manual were based on the latest PCB Revision available at the time of publishing of this manual. The motherboard you've received may or may not look exactly the same as the graphics shown in the manual.

Quick Reference (X6DH8-XG2/X6DHE-XG2)

(*Please refer to Chapter 2 for pin definitions and detailed information.)

<u>Jumper</u>	<u>Description</u>	<u>Default Setting</u>
J18	Watch Dog Enable	Pins 1-2 (Reset)
JP12	Power Fault	(See Chapter 2)
JP13	3rd PWR Supply PWR Fault Detect	On (Enabled)
JP14	Alarm Reset Enable	Off (Disabled)
JPA1	SCSI Controller Enable	On (Enabled)(*X6DH8)
JBT1	CMOS Clear	See Chapter 2
JPG1	VGA Enable	Pins 1-2 (Enabled)
JPL1	GLAN Enable/Disable	Pins 1-2 (Enabled)

<u>Connector</u>	<u>Description</u>
ATX PWR (JPW1)	Primary ATX PWR Connector(*Also:JPW2, JPW3)
CPU PWR (JPW3)	+12 V8-pin CPU PWR Connector (*required)
Aux. PWR (JPW2)	+12V 4-pin (Auxiliary) system PS (*Required)
COM1/COM2	COM1(JCOM1)/COM2(JCOM2) Serial Ports
DIMM#1A-DIMM#4B	Memory (RAM) Slots
FAN 1-8	CPU/Chassis/Overheat Fan Headers (Fan7: CPU1 Fan, Fan8:CPU2 Fan)
FP USB 2/3/4	Front Panel USB Headers(JUSB2: FPUSB 2/3, JUSB3: FPUSB 4)
GLAN 1/2	G-bit Ethernet Ports
IDE1/IDE2 (JIDE1/JIDE2)	IDE1/2 Hard Disk Drive Connectors
J22	System Management Bus Connector
J24	Power System Management Connector
JD1	PWR LED(pins1-3)/SpeakerHeader (pins 4-7)
JF1	Front Control Panel Connector
JL1	Chassis Intrusion Header
JPA2, JPA3	SCSI CHA(JPA2),CHB(JPA3)Term. Enabled(X6DH8)
JPFDD1	Floppy Disk Drive Connector
JPIMI	IPMI Connector
JOH1	Overheat LED
JVGA1	VGA Connector
JWOL	Wake-on-LAN Header
JWOR1	Wake-on-Ring Header
USB 0/1(JUSB1)	(Back Panel) Universal Serial Bus Ports
<u>Indicator</u>	<u>Description</u>
LE1	Power LED
DA1/DA2 (*X6DH8)	SCSI CH A LED (DA1)/SCSI CH B LED(DA2)

Motherboard Features

CPU

- Single or dual Intel® 604-pin Nocona™ (w/EM64T) processors at 800 MHz front side (system) bus speed. (***Notes:** CPU FSB speed is set by Manufacturer. Please do not change the FSB setting. Please refer to the support section of our web site for a complete listing of supported processors (<http://www.supermicro.com/products/>.)

Memory

- Eight 240-pin DIMM sockets supporting up to 16 GB Registered ECC DDR2-400 (PC3200) SDRAM
Notes: 1. Memory size is set via BIOS. 2. Interleaved memory; requires memory modules to be installed in pairs. See Section 2-3 for details.

Chipset

- Intel E7520 Lindenhurst chipset

Expansion Slots

- X6DH8-XG2/X6DHE-XG2
One PCI-E slot (x8@4GB/sec)
Five 64-bit PCI-X slots (*Three PCI-X-133 slots, One PCI-X-100 slot, One PCI-X-100 w/ZCR suport)

BIOS

- 8 Mb Phoenix® Flash ROM
- DMI 2.1, PCI 2.2, ACPI 2.0, Plug and Play (PnP), SMBIOS 2.3

PC Health Monitoring

- Onboard voltage monitors for CPU cores, chipset voltage, 3.3V, +5V, +12V and 3.3V standby
- Fan status monitor with firmware/software on/off control
- CPU/chassis temperature monitors
- Environmental temperature monitor and control
- CPU fan auto-off in sleep mode (S1 is not supported.)
- CPU slow-down on temperature overheat
- CPU thermal trip support for processor protection, +5V standby alert LED
- Power-up mode control for recovery from AC power loss

- System overheat LED and control
- Chassis intrusion detection
- System resource alert

ACPI Features (optional)

- Microsoft OnNow
- Slow blinking LED for suspend state indicator
- Main switch override mechanism

Onboard I/O

- Support Adaptec 7902 dual channel Ultra 320 SCSI(*X6DH8-XG2 only)
- One IPMI 2.0 socket
- Intel 82546 dual Channel Gigabit Ethernet controller (PCI-X-133)
- 2 EIDE Ultra DMA/100 bus master interfaces
- 1 floppy port interface (up to 2.88 MB)
- PS/2 mouse and PS/2 keyboard ports
- Up to 5 USB 2.0 (Universal Serial Bus) (3 Front Panel USB ports, 2 Back Panel USB Headers)
- Super I/O
- 2 COM ports (1 header, 1 port)
- 2 SATA ports support 2 drives (RAID 0, 1)

Other

- Internal/external modem ring-on (WOR)
- Wake-on-LAN (WOL)
- Console redirection
- SMBbus for SMC Power Supply
- Fan Speed Control via BIOS

CD/Diskette Utilities

- BIOS flash upgrade utility and device drivers

Dimensions

- ATX Ext. 12" x 13.05" (304.8 x 331.5 mm)

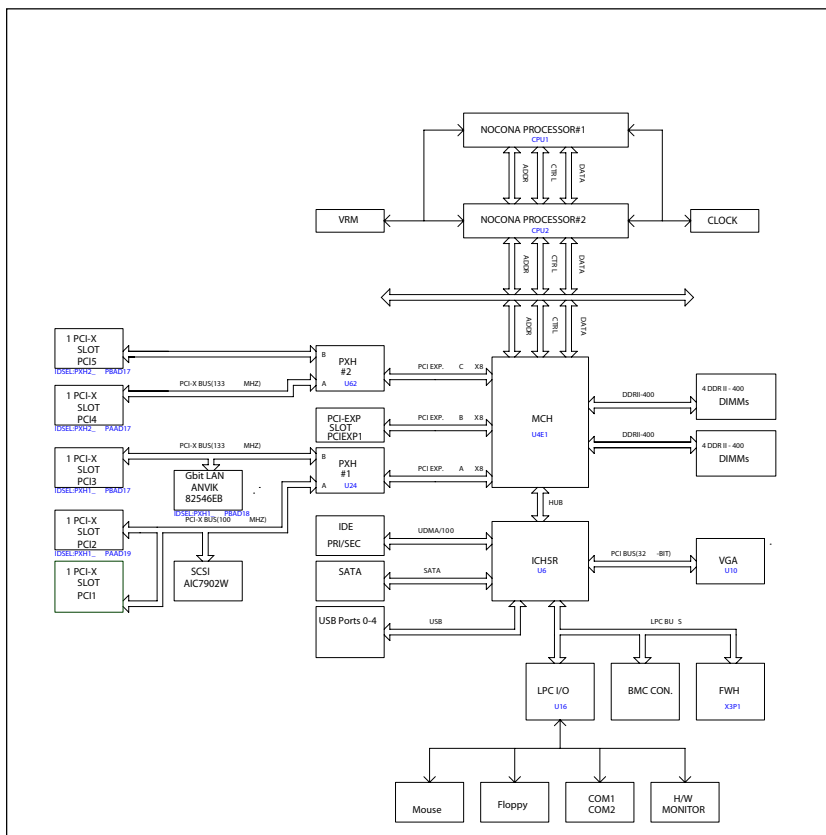


Figure 1-9. Block Diagram of the E7520 Lindenhurst Chipset

Note: This is a general block diagram. Please see the previous Motherboard Features pages for details on the features of each motherboard.

1-2 Chipset Overview

Built upon the functionality and the capability of the E7520 Lindenhurst chipset, The X6DH8-XG2/X6DHE-XG2 motherboard provides the performance and feature set required for dual processor-based servers, with configuration options optimized for communications, presentation, storage, computation or database applications. The Intel E7520 Lindenhurst chipset consists of the following components: the Lindenhurst Memory Controller Hub (MCH), the 82801ER I/O Controller Hub 5-R (ICH5-R), and the PCI-X Hub.

The E7520 Lindenhurst MCH supports single or dual Nocona processors with Front Side Bus speeds of up to 800 MHz(*Note). Its memory controller provides direct connection to two channels of registered DDR2 400 with a marched system bus address and data bandwidths of up to 6.4GB/s. The Lindenhurst also supports the new PCI Express high speed serial I/O interface for superior I/O bandwidth. The MCH provides three configurable x8 PCI Express interfaces which may alternatively be configured as two independent x4 PCI Express interfaces. These interfaces support connection of the MCH to a variety of other bridges that are compliant with the PCI Express Interface Specification, Rev. 1.0a. The MCH interfaces with the 82801ER I/O Controller Hub 5-R (ICH5R) via a dedicated Hub Interface supporting a peak bandwidth of 266 MB/s using a x4 base clock of 66 MHz. The PXH provides connection between a PCI Express interface and two independent PCI bus interfaces that can be configured for standard PCI 2.3 protocol, as well as the enhanced high-frequency PCI-X protocol. The PXH can be configured to support for 64-bit PCI devices running at 33 MHz, 66 MHz, 100 MHz, and 133 MHz.

The ICH5R I/O Controller Hub provides legacy support similar to that of previous ICH-family devices, but with extensions in RAID 0,1 support, Serial ATA Technology, and an integrated ASF Controller. In addition, the ICH5R also provides various integrated functions, including a two-channel Ultra ATA/100 bus master IDE controller, USB 2.0 host controllers, an integrated 10/100 LAN controller, an LPC firmware hub (FWH) and Super IO interface, a System Management Interface, a power management interface, integrated IOxAPIC and 8259 interrupt controllers.

(*Notes: The CPU FSB speed is set at 800 MHz by the Manufacturer.
Please do not change the CPU FSB setting.)

1-3 Special Features

Recovery from AC Power Loss

BIOS provides a setting for you to determine how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must hit the power switch to turn it back on) or for it to automatically return to a power-on state. See the Power Lost Control setting in the Advanced BIOS Setup section to change this setting. The default setting is Last State.

1-4 PC Health Monitoring

This section describes the PC health monitoring features of the X6DH8-XG2/X6DHE-XG2. All have an onboard System Hardware Monitor chip that supports PC health monitoring.

Onboard Voltage Monitors for the CPU Cores, Chipset Voltage, +3.3V, +5V, +12V and +3.3V Standby

An onboard voltage monitor will scan these voltages continuously. Once a voltage becomes unstable, a warning is given or an error message is sent to the screen. Users can adjust the voltage thresholds to define the sensitivity of the voltage monitor.

Fan Status Monitor with Firmware/Software On/Off Control

The PC health monitor can check the RPM status of the cooling fans. The onboard CPU and chassis fans are controlled by the power management functions. The thermal fan is controlled by the overheat detection logic.

Environmental Temperature Control

The thermal control sensor monitors the CPU temperature in real time and will turn on the thermal control fan whenever the CPU temperature exceeds a user-defined threshold. The overheat circuitry runs independently from the CPU. It can continue to monitor for overheat conditions even when the CPU is in sleep mode. Once it detects that the CPU temperature is too high, it will automatically turn on the thermal control fan to prevent any overheat damage to the CPU. The onboard chassis thermal circuitry can monitor the overall system temperature and alert users when the chassis temperature is too high.

CPU Fan Auto-Off in Sleep Mode

The CPU fan activates when the power is turned on. It continues to operate when the system enters Standby mode. When in sleep mode, the CPU will not run at full power, thereby generating less heat.

CPU Overheat LED and Control

This feature is available when the user enables the CPU overheat warning function in the BIOS. This allows the user to define an overheat temperature. When this temperature is exceeded, both the overheat fan and the warning LED are triggered.

System Resource Alert

This feature is available when used with Supero Doctor III in the Windows OS environment or used with Supero Doctor II in Linux. LDCM is used to notify the user of certain system events. For example, if the system is running low on virtual memory and there is insufficient hard drive space for saving the data, you can be alerted of the potential problem.

1-5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a PC system, including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as CD-ROMs, network cards, hard disk drives and printers. This also includes consumer devices connected to the PC such as VCRs, TVs, telephones and stereos.

In addition to enabling operating system-directed power management, ACPI provides a generic system event mechanism for Plug and Play and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures while providing a processor architecture-independent implementation that is compatible with both Windows 2000 and Windows 2003.

Slow Blinking LED for Suspend-State Indicator

When the CPU goes into a suspend state, the chassis power LED will start blinking to indicate that the CPU is in suspend mode. When the user presses any key, the CPU will wake-up and the LED will automatically stop blinking and remain on.

Main Switch Override Mechanism

When an ATX power supply is used, the power button can function as a system suspend button to make the system enter a SoftOff state. The monitor will be suspended and the hard drive will spin down. Pressing the power button again will cause the whole system to wake-up. During the SoftOff state, the ATX power supply provides power to keep the required circuitry in the system alive. In case the system malfunctions and you want to turn off the power, just press and hold the power button for 4 seconds. This option can be set in the Power section of the BIOS Setup routine.

External Modem Ring-On

Wake-up events can be triggered by a device such as the external modem ringing when the system is in the SoftOff state. Note that external modem ring-on can only be used with an ATX 2.01 (or above) compliant power supply.

Wake-On-LAN (WOL)

Wake-On-LAN is defined as the ability of a management application to remotely power up a computer that is powered off. Remote PC setup, updates and asset tracking can occur after hours and on weekends so that daily LAN traffic is kept to a minimum and users are not interrupted. The motherboard has a 3-pin header (WOL) to connect to the 3-pin header on a Network Interface Card (NIC) that has WOL capability. Wake-On-LAN must be enabled in BIOS. Note that Wake-On-LAN can only be used with an ATX 2.01 (or above) compliant power supply.

1-6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates.

The X6DH8-XG2/X6DHE-XG2 accommodates ATX power supplies. Although most power supplies generally meet the specifications required by the CPU, some are inadequate. You should use one that will supply at least 400W of power and includes the additional +12V, 4-pin power connector - an even higher wattage power supply is recommended for high-load configurations. Also your power supply must supply 1.5A for the Ethernet ports.

NOTE: A 12V 8-pin power connector (JPW3) is required for CPU power support, and additional 12 V 4-pin power connector (JPW2) is also required to supplement system power consumption. Failure to provide this extra power will result in instability of the CPU after only a few minutes of operation. See Section 2-5 for details on connecting the power supply.

It is strongly recommended that you use a high quality power supply that meets ATX power supply Specification 2.02 or above. It must also be SSI compliant (info at <http://www.ssiforum.org/>). Additionally, in areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

1-7 Super I/O

The disk drive adapter functions of the Super I/O chip include a floppy disk drive controller that is compatible with industry standard 82077/765, a data separator, write pre-compensation circuitry, decode logic, data rate selection, a clock generator, drive interface control logic and interrupt and DMA logic. The wide range of functions integrated onto the Super I/O greatly reduces the number of components required for interfacing with floppy disk drives. The Super I/O supports 360 K, 720 K, 1.2 M, 1.44 M or 2.88 M disk drives and data transfer rates of 250 Kb/s, 500 Kb/s or 1 Mb/s. It also provides two high-speed, 16550 compatible serial communication ports (UARTs). Each UART includes a 16-byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt system. Both UARTs provide legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O supports one PC-compatible printer port (SPP), Bi-directional Printer Port (BPP), Enhanced Parallel Port (EPP) or Extended Capabilities Port (ECP).

The Super I/O provides functions that comply with ACPI (Advanced Configuration and Power Interface), which includes support of legacy and ACPI power management through an SMI or SCI function pin. It also features auto power management to reduce power consumption.

Chapter 2

Installation

2-1 Static-Sensitive Devices

Electric-Static-Discharge (ESD) can damage electronic components. To prevent damage to your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the anti-static bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of onboard CMOS battery. Do not install the onboard upside down battery to avoid possible explosion.

Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure the person handling it is static protected.

2-2 Processor and Heatsink Installation



When handling the processor package, avoid placing direct pressure on the label area of the fan. Also, do not place the motherboard on a conductive surface, which can damage the BIOS battery and prevent the system from booting up.

IMPORTANT: Always connect the power cord last and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket **before** you install the CPU (CEK) heat sink. To adequately support the weight of CPU heat sinks, please install the X6DH8-XG2/X6DHE-XG2 in a chassis that is compliant with the SSI EEB 3.5 Specification.

CPU Installation

1. Lift the lever on the CPU socket:
lift the lever completely as shown on the picture on the right; otherwise, you will damage the CPU socket when power is applied. (Install CPU1 first.)



Socket lever Pin1

2. Insert the CPU in the socket, making sure that pin 1 of the CPU aligns with pin 1 of the socket (both corners are marked with a triangle). When using only one CPU, install it into CPU socket #1 (socket #2 is automatically disabled if only one CPU is used).



Pin 1

3. Press the lever down until you hear the *click* so you can be sure that the CPU is securely installed in the CPU socket.

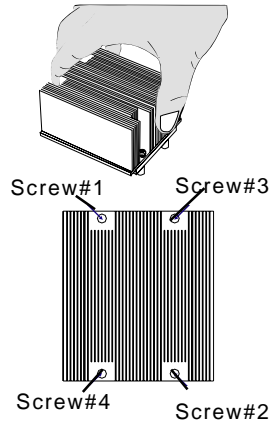
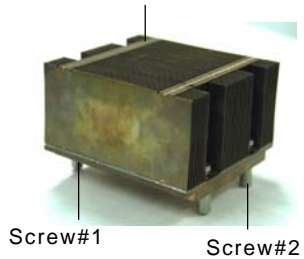


Socket lever in the locking Position

Heatsink Installation

1. Do not apply any thermal compound to the heatsink or the CPU die-the required amount has already been applied.
2. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the retention mechanism.
3. Screw in two diagonal screws (ie the #1 and the #2 screws) until just snug (- do not fully tighten the screws to avoid possible damage to the CPU.)
4. Finish the installation by fully tightening all four screws.

Intel CEK Heatsink

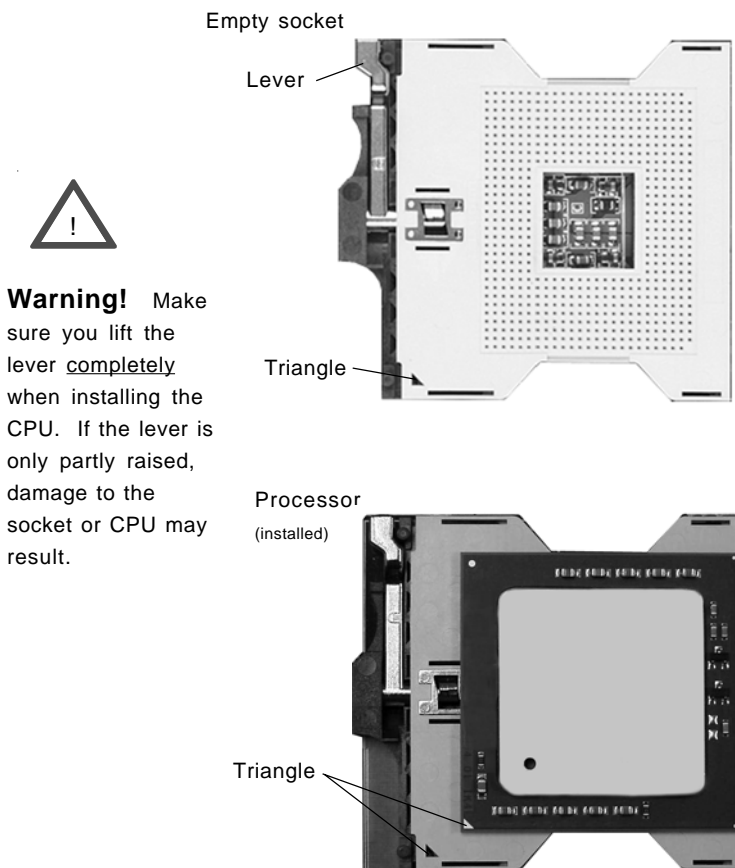


Heatsink Removal

(Caution! We do not recommend that the CPU or the heatsink be removed. However, if you do need to un-install the heatsink, please follow the instructions below to remove the heatsink to prevent damage done to the CPU or the CPU socket.)

1. Unscrew and remove the heatsink screws from the motherboard in the sequence as show in the picture on the right.
2. Hold the heatsink as show in the picture on the right and gently wriggle the heatsink to loosen it from the CPU. (Do not use excessive force when wriggling the heatsink!!)
3. Once the CPU is loosened from the heatsink, remove the heatsink from the CPU socket.
4. Clean the surface of the CPU and the heatsink to get rid of the old thermal grease. Reapply the proper amount of thermal grease on the surface before you reinstall the CPU and the heatsink.

Figure 2-1. PGA 604 Socket: Empty and with Processor Installed



Mounting the Motherboard in the Chassis

All motherboards have standard mounting holes to fit different types of chassis. Make sure the location of all the mounting holes for both the motherboard and the chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.

2-3 Installing DIMMs

Note: Check the Supermicro web site for recommended memory modules:
http://www.supermicro.com/TECHSUPPORT/FAQs/Memory_vendors.htm

CAUTION

Exercise extreme care when installing or removing DIMM modules to prevent any possible damage. Also note that the memory is interleaved to improve performance (see step 1).

DIMM Installation (See Figure 2-2)

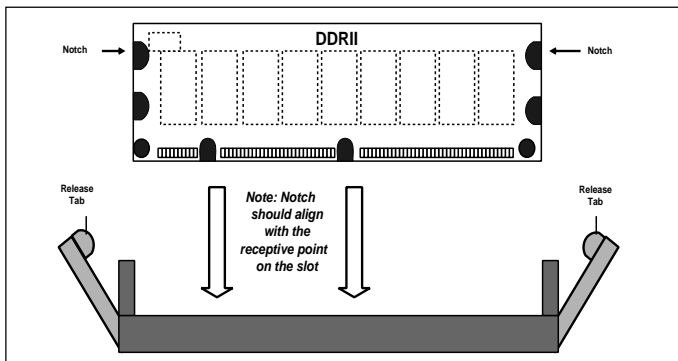
1. Insert the desired number of DIMMs into the memory slots, starting with Bank 0. The memory scheme is interleaved so you must install two modules at a time, beginning with Bank 1, then Bank 2, and so on.
2. Insert each DIMM module vertically into its slot. Pay attention to the notch along the bottom of the module to prevent inserting the DIMM module incorrectly.
3. Gently press down on the DIMM module until it snaps into place in the slot. Repeat for all modules (see step 1 above).

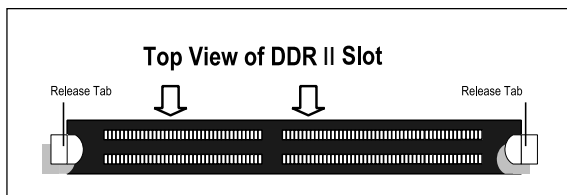
Memory Support

The X6DH8-XG2/X6DHE-XG2 supports up to 16 GB of Registered DDR2-400 (PC3200) memory. All motherboards were designed to support 2 GB modules in each slot, but has only been verified for up to 1 GB modules.

Figure 2-2. Installing and Removing DIMMs

To Install:
 Insert module vertically and press down until it snaps into place. Pay attention to the alignment notch at the bottom.





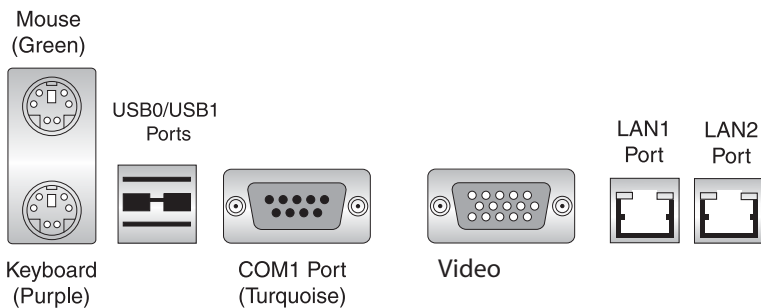
To Remove:

Use your thumbs to gently push near the edge of both ends of the module. This should release it from the slot.

2-4 I/O Ports/Control Panel Connectors

The I/O ports are color coded in conformance with the PC 99 specification. See Figure 2-3 below for the colors and locations of the various I/O ports.

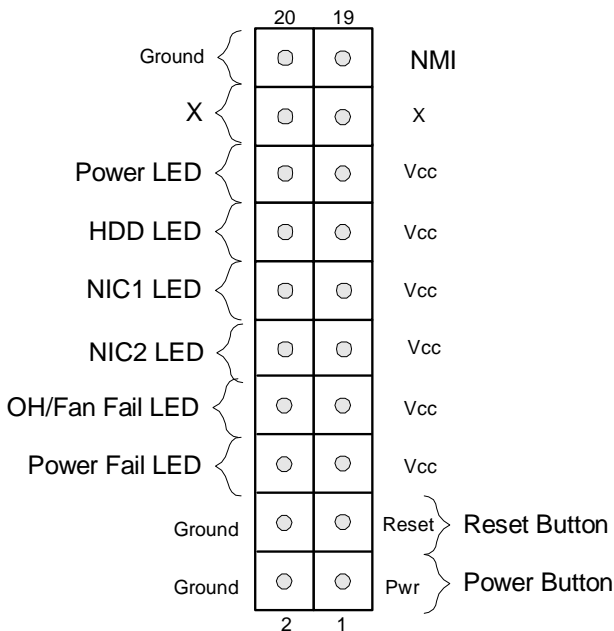
Figure 2-3. I/O Port Locations and Definitions



Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro server chassis. See Figure 2-4 for the descriptions of the various control panel buttons and LED indicators. Refer to the following section for descriptions and pin definitions.

Figure 2-4. JF1 Header Pins



2-5 Connecting Cables

ATX Power Connector

The main power supply connector on the X6DH8-XG2/X6DHE-XG2 meets the SSI (Superset ATX) 24-pin specification. You must also connect the 4-pin (JPW2) power connector to your power supply to provide adequate power supply for system power consumption. See the table on the right for pin definitions.

ATX Power Supply 24-pin Connector
Pin Definitions (JPW1)

Pin Number	Definition	Pin Number	Definition
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	COM	3	COM
16	PS_ON#	4	+5V
17	COM	5	COM
18	COM	6	+5V
19	COM	7	COM
20	Res(NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	COM	12	+3.3V

+12V 4-pin
Connector
(JPW2)

Pins #	Definition
1 & 2	Ground
3 & 4	+12 V

(*Required)

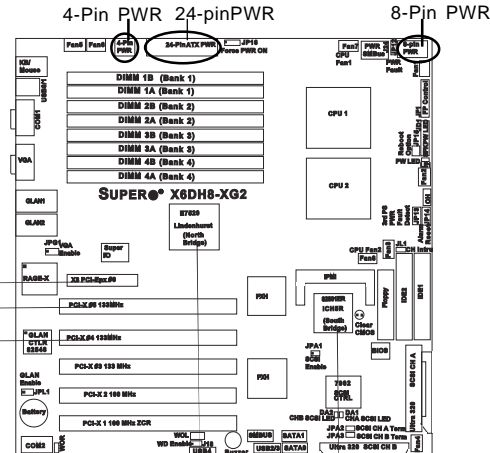
Processor Power Connector

In addition to the Primary ATX and the Auxiliary power connectors (above), the 12v 8-pin Processor connector at JPW3 must also be connected to your power supply for CPU power consumption.

+12v 8-Pin Power Supply
Connector (J1D1)

Pins	Definition
1 thru 4	Ground
5 thru 8	+12v

(*Required)



NMI Button

The non-maskable interrupt button header is located on pins 19 and 20 of JF1. Refer to the table on the right for pin definitions.

NMI Button Pin Definitions (JF1)

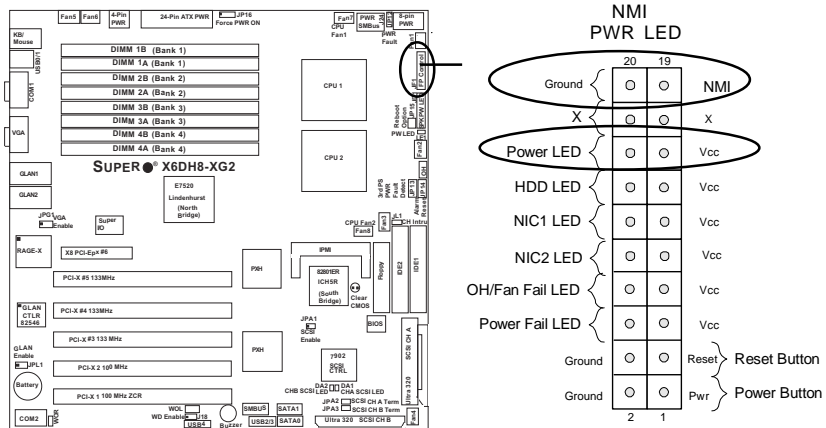
Pin Number	Definition
19	Control
20	Ground

Power LED

The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table on the right for pin definitions.

PWR_LED Pin Definitions (JF1)

Pin Number	Definition
15	Vcc
16	Control



HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. Attach the hard drive LED cable here to display disk activity (for any hard drives on the system, including SCSI, Serial ATA and IDE). See the table on the right for pin definitions.

HDD LED Pin Definitions (JF1)

Pin Number	Definition
13	Vcc
14	HD Active

NIC1/NIC2 LED Indicators

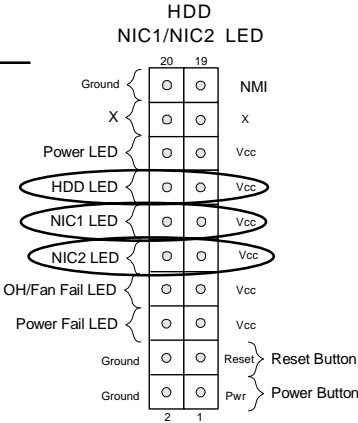
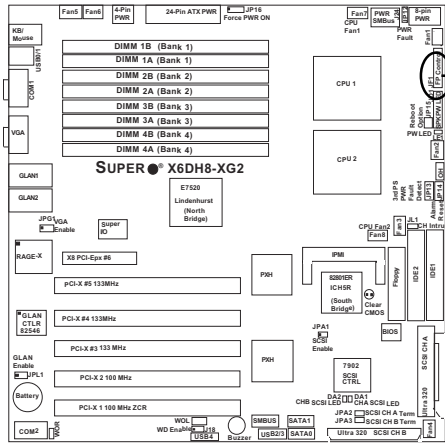
The NIC (Network Interface Controller) LED connections for the GLAN Port1 is located on pins 11 and 12 of JF1, and for the GLAN Port2 is located on pins 9 and 10 of JF1. Attach the NIC LED cables to display network activity. Refer to the tables on the right for pin definitions.

NIC1 LED Pin Definitions (JF1)

Pin Number	Definition
11	Vcc
12	GND

NIC2 LED Pin Definitions (JF1)

Pin Number	Definition
9	Vcc
10	GND



Overheat/FanFail LED

Connect an LED to the OH/Fan Fail connection on pins 7 and 8 of JF1 to provide advanced warning of chassis overheating or system fan failure. Refer to the table on the right for pin definitions.

**OH/Fan Fail LED
Pin Definitions
(JF1)**

Pin Number	Definition
7	Vcc
8	GND

**OH/Fan Fail LED
(JF1)**

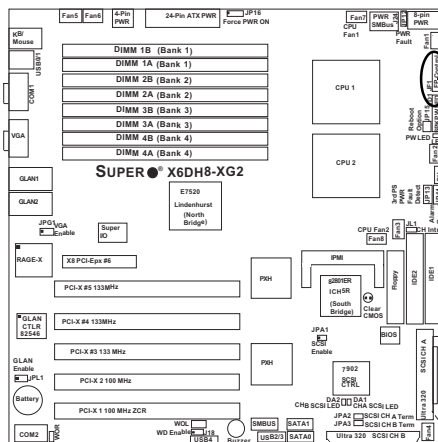
State	Message
Off	Normal
Stay On	Overheat
Blink	Fan Fail

Power Fail LED

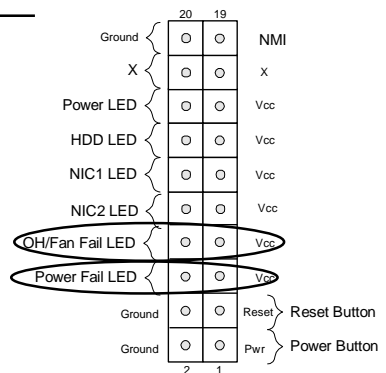
The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the table on the right for pin definitions.

**Power Fail LED Pin
Definitions
(JF1)**

Pin Number	Definition
5	Vcc
6	GND



**OH/Fan Fail LED
PWR Fail LED**



Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to the hardware reset switch on the computer case. Refer to the table on the right for pin definitions.

Reset Pin Definitions (JF1)

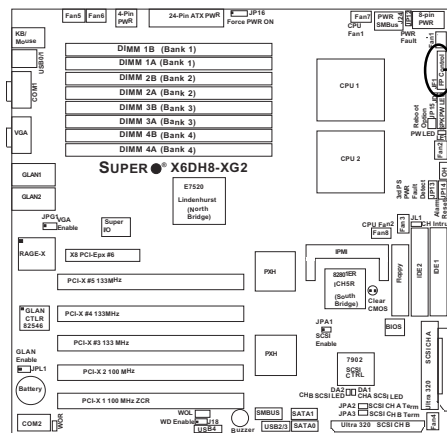
Pin Number	Definition
3	Reset
4	Ground

Power Button

The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (with a setting in BIOS - see Chapter 4). To turn off the power when set to suspend mode, press the button for at least 4 seconds. Refer to the table on the right for pin definitions.

Power Button Connector Pin Definitions (JF1)

Pin Number	Definition
1	PW_ON
2	Ground



Reset Button
PWR Button

	20	19	
Ground	○	○	NMI
X	○	○	X
Power LED	○	○	Vcc
HDD LED	○	○	Vcc
NIC1 LED	○	○	Vcc
NIC2 LED	○	○	Vcc
OH/Fan Fail LED	○	○	Vcc
Power Fail LED	○	○	Vcc
Ground	○	○	Reset
Ground	○	○	Pwr

Reset Button

Power Button

Chassis Intrusion

A Chassis Intrusion header is located at JL1. Attach the appropriate cable to inform you of a chassis intrusion.

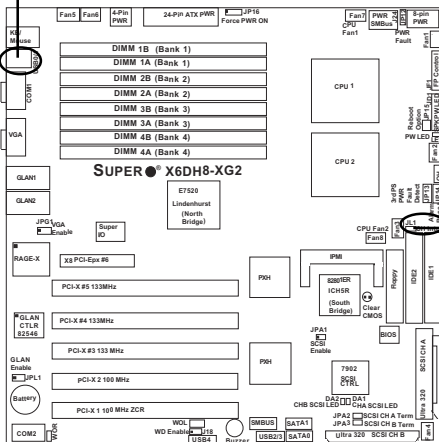
Universal Serial Bus (USB0/1)

Two USB 2.0 ports (JPUSB1) are located beside the PS/2 keyboard/mouse ports. USB0 is the bottom connector and USB1 is the top connector. See the table on the right for pin definitions.

Universal Serial Bus Pin Definitions
USB0 USB1

Pin Number	Definition	Pin Number	Definition
1	+5V	1	+5V
2	P0-	2	P0-
3	P0+	3	P0+
4	Ground	4	Ground
5	N/A	5	Key

USB 0/1



Chassis
Intrusion

**Front Panel Universal
Serial Bus Headers**

Extra USB header: FP USB2/FP USB3 (JUSB2), and FP USB4 (JUSB3) can be used for front side USB access. You will need a USB cable to use either connection. Refer to the tables on the right for pin definitions.

**Front Panel Universal Serial Bus Pin
Definitions**

FPUSB2/FPUSB3		FPUSB4	
Pin Number	Definition	Pin Number	Definition
1	+5V	1	+5V
2	P0-	2	P0-
3	P0+	3	P0+
4	Ground	4	Ground
5	N/A	5	Key

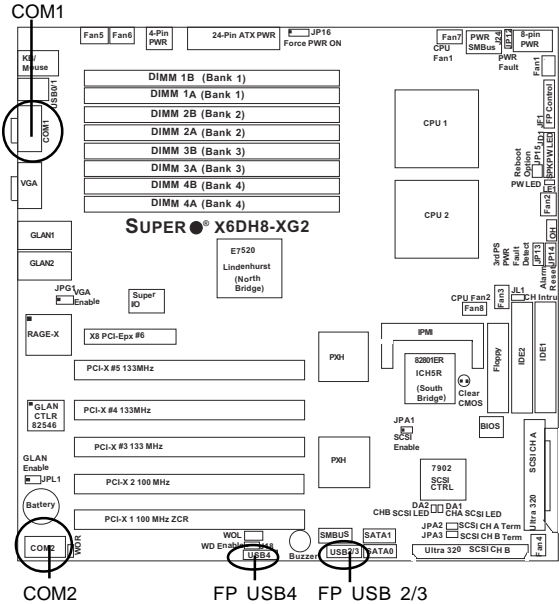
Serial Ports

The COM1 (JCOM1) and COM2 (JCOM2) serial ports are located under the parallel port (see Figure 2-3). See the table on the right for pin definitions.

**Serial Port Pin Definitions
(COM1, COM2)**

Pin Number	Definition	Pin Number	Definition
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTS
4	DTR	9	RI
5	GND	10	NC

(*Pin 10 is available on COM2 only.)



GLAN (Giga-bit Ethernet Ports)



A G-bit Ethernet port (designated JLAN1/JLAN2) is located beside the COM2 port on the IO back-plane. This port accepts RJ45 type cables.

ATX PS/2 Keyboard and PS/2 Mouse Ports

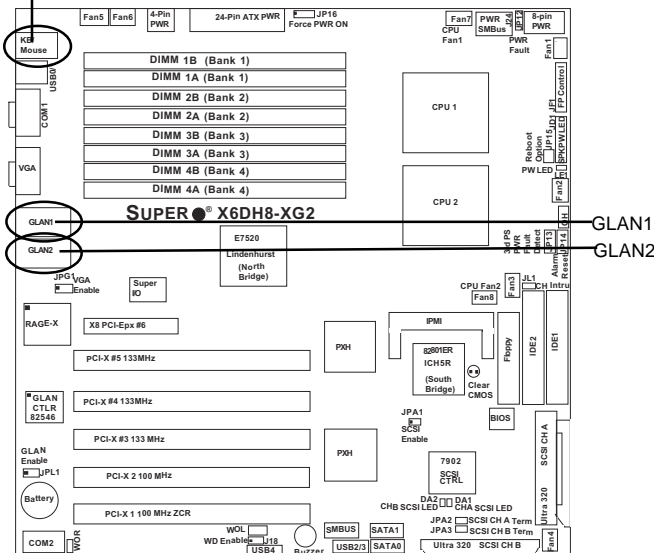
The ATX PS/2 keyboard and PS/2 mouse are located at J9. See the table at right for pin definitions. (See Figure 2-3 for the locations of each.)

PS/2 Keyboard and Mouse Port Pin Definitions (JKM1)

Pin Number	Definition
1	Data
2	NC
3	Ground
4	VCC
5	Clock
6	NC

Keyboard/

Mouse



Fan Headers

The X6DH8-XG2/X6DHE-XG2 has eight fan headers. (*Note: These fan headers can support both 3-pin fans and 4-pin fans. Pins #1-#3 of 4-pin fan headers are backward compatible with the traditional 3-pin fans.) The fan speeds are controlled by Thermal Management via BIOS under the Hardware Monitoring section in the Advanced Setting. See the table on the right for pin definitions.

4-pin Fan Header Pin Definitions (CPU and Chassis Fans)

Pin#	Definition
1	Ground (black)
2	+12V (red)
3	Tachometer
4	PWR_Control

Caution: These fan headers use DC power.

Fan Header Pin Definitions

Pin Number	Definition
1	Ground (black)
2	+12V (red)
3	Tachometer

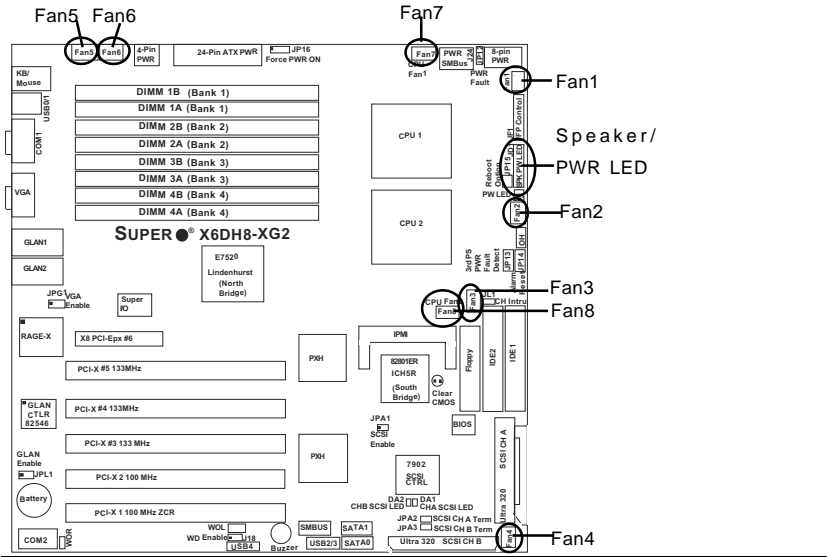
Caution: Fan headers are DC power.

Power LED/Speaker

On the JDI header, pins 1-3 are for a power LED and pins 4-7 are for the speaker. See the table on the right for speaker pin definitions. Note: The speaker connector pins are for use with an external speaker. If you wish to use the onboard speaker, you should close pins 6-7 with a jumper.

Speaker Connector Pin Definitions (JDI)

Pin Number	Function	Definition
4	+	Red wire, Speaker data
5	Key	No connection
6		Key
7		Speaker data



Wake-On-Ring

The Wake-On-Ring header is designated JWOR1. This function allows your computer to receive and to be awakened up by an incoming call to the modem when in suspend state. See the table on the right for pin definitions. You must have a Wake-On-Ring card and cable to use this feature.

Wake-on-Ring Pin Definitions (JWOR1)

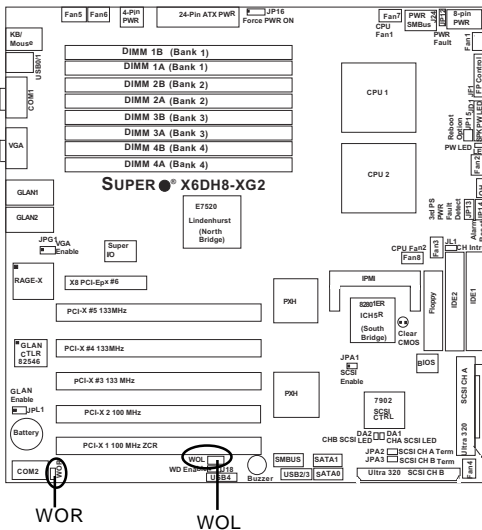
Pin Number	Definition
1	Ground
2	Wake-up

Wake-On-LAN

The Wake-On-LAN header is located at JWOL on the motherboard. See the table on the right for pin definitions. You must enable LAN Wake-Up in the BIOS to use this function. (You must also have a LAN card with a Wake-On-LAN connector and cable to use this feature.)

Wake-On-LAN Pin Definitions (JWOL)

Pin Number	Definition
1	+5V Standby
2	Ground
3	Wake-up



Power Fault

Connect a cable from your power supply to the Power Fault header (JP12) to provide warnings of power supply failure. This warning signal is passed through the PWR_LED pin to indicate of a power failure on the chassis. See the table on the right for pin definitions.

Power Fault
Pin Definitions (JP12)

Pin Number	Definition
1	PWR 1 Fail Signal
2	PWR 2 Fail Signal
3	PWR 3 Fail Signal
4	Signal: Alarm Reset

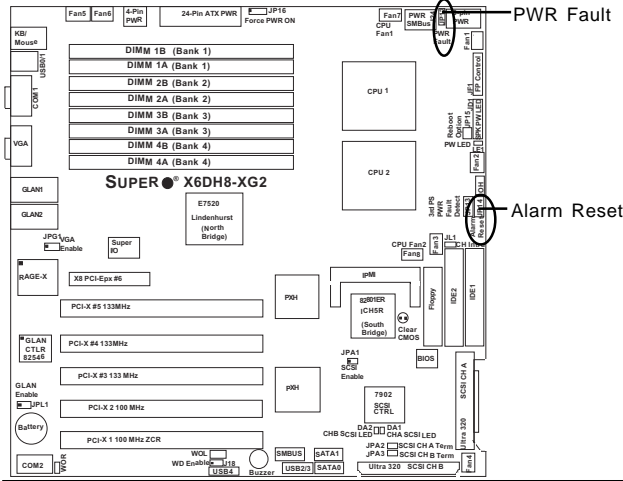
Note: This feature is only available when using redundant Supermicro power supplies.

Power Fail Alarm Clear
Switch (Alarm Reset)

The system will notify you in the event of a power supply failure. To use this feature, you will need to have the Supermicro redundant power supply units installed in the chassis. If you only have a single power supply installed, you should leave the pins open (the default setting) to prevent false alarms. See the table on the right for jumper settings.

Alarm Clear Switch
(JP14)

Jumper Position	Definition
Open	Normal(*default)
Short	Clear Alarm



Overheat LED (JOH1)

The JOH1 header is used to connect an LED to provide warnings of chassis overheating. It is located near the microphone connector.

Overheat LED
Pin Definitions (JOH1)

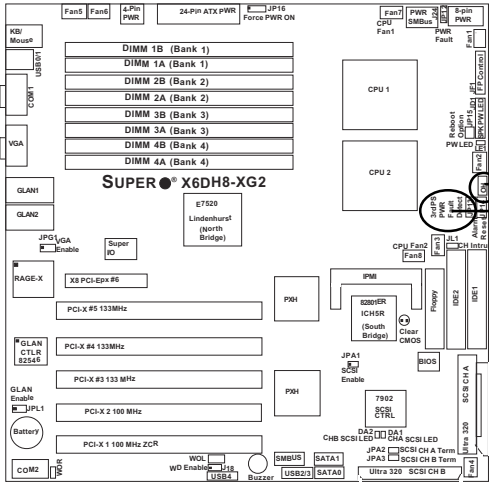
Pin Number	Definition
1	5vDC
2	OH Active

3rd PWR Supply PWR
Fault Detect (JP13)

The system can notify you in the event of a power supply failure. This feature assumes that three power supply units are installed in the chassis, with one acting as a backup. If you only have one or two power supply units installed, you should disable this (the default setting) with JP13 to prevent false alarms.

3rd PWR Supply PWR
Fault Detect
Jumper Settings (JP13)

Jumper Position	Definition
Open	Disabled
Closed	Enabled

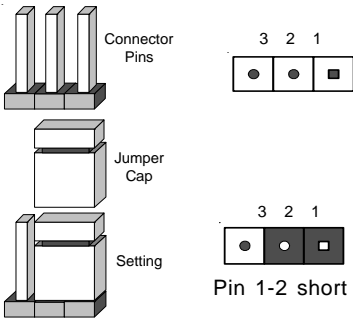


2-6 Jumper Settings

Explanation of Jumpers

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the motherboard layout pages for jumper locations.

Note: On two pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.

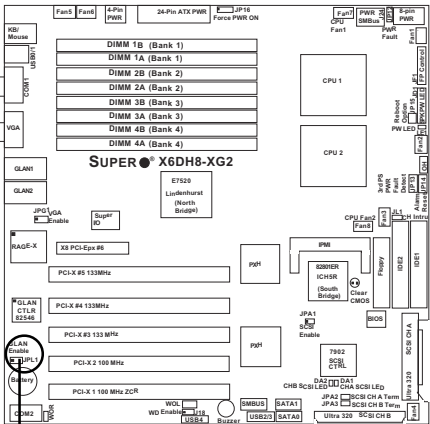


GLAN Enable/Disable

JPL1 enables or disables the GLAN port(s) on the motherboard. See the table on the right for jumper settings. The default setting is enabled.

GLAN
Enable/Disable
Jumper Settings
(JPL1)

Jumper Position	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled



CMOS Clear

JBT1 is used to clear CMOS. Instead of pins, this "jumper" consists of contact pads to prevent the accidental clearing of CMOS. To clear CMOS, use a metal object such as a small screwdriver to touch both pads at the same time to short the connection. Always remove the AC power cord from the system before clearing CMOS.

Note: For an ATX power supply, you must completely shut down the system, remove the AC power cord and then short JBT1 to clear CMOS. Do not use the PW ON connector to clear CMOS.

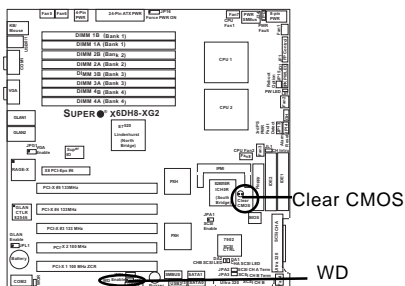


Watch Dog

J18 controls Watch Dog, a system monitor that takes action when a software application hangs. Close Pins 1-2 to reset the system if a program hangs. Close Pins 2-3 to generate a non-maskable interrupt for the program that hangs. Watch Dog must also be enabled in BIOS.

**Watch Dog
Jumper Settings (J18)**

Jumper Position	Definition
Pins 1-2	WD to Reset
Pins 2-3	WD to NMI
Open	Disabled



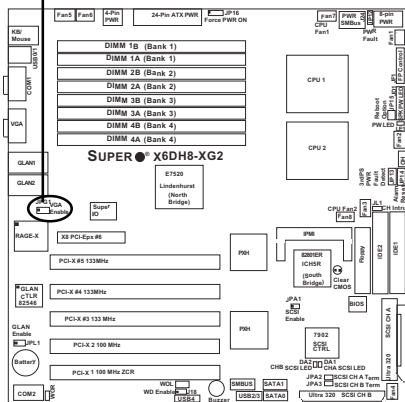
VGA Enable/Disable

JPG1 enables or disables the VGA Connector on the motherboard. See the table on the right for jumper settings. The default setting is enabled.

**VGA
Enable/Disable
Jumper Settings
(JPG1)**

Jumper Position	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

VGA Enable



SCSI Enable/Disable
(*ForX6DH8-XG2 only)

Jumper JPA1 allows you to enable or disable the SCSI headers. Jumper JPA1 is for headers #1 and #2. The default setting is pins 1-2 to enable all four headers. See the table on the right for jumper settings.

**SCSI Enable/Disable
Jumper Settings
(JPA1)**

Jumper Position	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

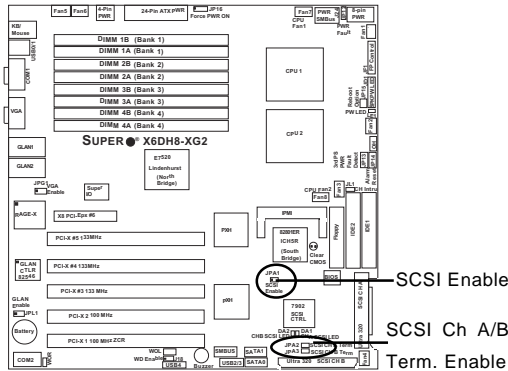
**SCSI Termination Enable/
Disable (*ForX6DH8-XG2
only)**

Jumpers JPA2 and JPA3 allow you to enable or disable termination for the SCSI connectors. Jumper JPA2 controls SCSI channel A and JPA3 is for SCSI channel B. The default setting is open to enable (terminate) both SCSI channels. (*Note: For the SCSI Drives to function properly, please do not change the default setting.) See the table on the right for jumper settings.

**SCSI Channel Termination
Enable (JPA2, JPA3)**

Jumper Position	Definition
*Open Closed	Enabled Disabled

(*Default)



2-8 Floppy/Hard Disk Drive and SCSI Connections

Note the following when connecting the floppy and hard disk drive cables:

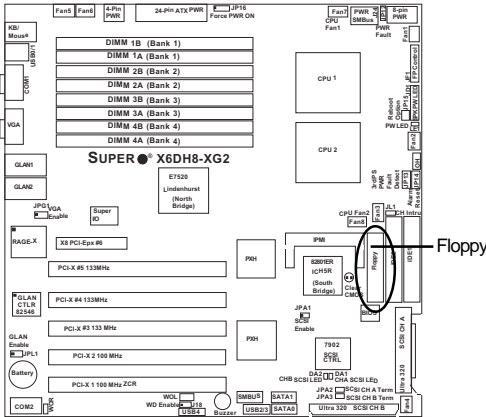
- The floppy disk drive cable has seven twisted wires.
- A red mark on a wire typically designates the location of pin 1.
- A single floppy disk drive ribbon cable has two connectors to provide for two floppy disk drives. The connector with twisted wires always connects to drive A, and the connector that does not have twisted wires always connects to drive B.

Floppy Connector

The floppy connector is located at JFDD1. See the table below for pin definitions.

Floppy Connector Pin Definitions

Pin Number	Function	Pin Number	Function
1	GND	2	FDHDIN
3	GND	4	Reserved
5	Key	6	FDEDIN
7	GND	8	Index-
9	GND	10	Motor Enable
11	GND	12	Drive Select B-
13	GND	14	Drive Select A-
15	GND	16	Motor Enable
17	GND	18	DIR-
19	GND	20	STEP-
21	GND	22	Write Data-
23	GND	24	Write Gate-
25	GND	26	Track 00-
27	GND	28	Write Protect-
29	GND	30	Read Data-
31	GND	32	Side 1 Select-
33	GND	34	Diskette

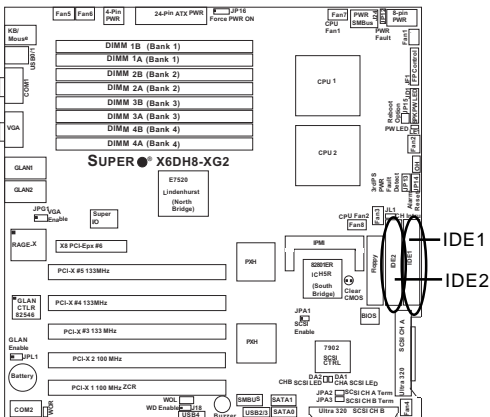


IDE Connectors

There are no jumpers to configure the onboard IDE#1 and #2 connectors (at J3 and J4, respectively). See the table on the right for pin definitions.

IDE Connector Pin Definitions

Pin Number	Function	Pin Number	Function
1	Reset IDE	2	GND
3	Host Data 7	4	Host Data 8
5	Host Data 6	6	Host Data 9
7	Host Data 5	8	Host Data 10
9	Host Data 4	10	Host Data 11
11	Host Data 3	12	Host Data 12
13	Host Data 2	14	Host Data 13
15	Host Data 1	16	Host Data 14
17	Host Data 0	18	Host Data 15
19	GND	20	Key
21	DRQ3	22	GND
23	I/O Write-	24	GND
25	I/O Read-	26	GND
27	IOCHRDY	28	BALE
29	DACK3-	30	GND
31	IRQ14	32	IOCS16-
33	Addr 1	34	GND
35	Addr 0	36	Addr 2
37	Chip Select 0	38	Chip Select 1-
39	Activity	40	GND

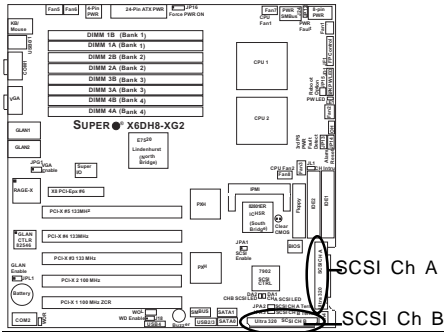


Ultra320 SCSI Connectors
(*X6DH8-XG2 Only)

Refer to the table below for the pin definitions of the Ultra320 SCSI connectors located at JA1 and JA2.

68-pin Ultra320 SCSI Connectors (JA1 and JA2)

Connector Contact Number	Signal Names	Connector Contact Number	Signal Names
1	+DB(12)	35	-DB(12)
2	+DB(13)	36	-DB(13)
3	+DB(14)	37	-DB(14)
4	+DB(15)	38	-DB(15)
5	+DB(P1)	39	-DB(P1)
6	+DB(0)	40	-DB(0)
7	+DB(1)	41	-DB(1)
8	+DB(2)	42	-DB(2)
9	+DB(3)	43	-DB(3)
10	+DB(4)	44	-DB(4)
11	+DB(5)	45	-DB(5)
12	+DB(6)	46	-DB(6)
13	+DB(7)	47	-DB(7)
14	+DB(P)	48	-DB(P)
15	GROUND	49	GROUND
16	DIFFSENS	50	GROUND
17	TERMPWR	51	TERMPWR
18	TERMPWR	52	TERMPWR
19	RESERVED	53	RESERVED
20	GROUND	54	GROUND
21	+ATN	55	-ATN
22	GROUND	56	GROUND
23	+BSY	57	-BSY
24	+ACK	58	-ACK
25	+RST	59	-RST
26	+MSG	60	-MSG
27	+SEL	61	-SEL
28	+C/D	62	-C/D
29	+REQ	63	-REQ
30	+I/O	64	-I/O
31	+DB(8)	65	-DB(8)
32	+DB(9)	66	-DB(9)
33	+DB(10)	67	-DB(10)
34	+DB(11)	68	-DB(11)



Chapter 3

Troubleshooting

3-1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter.

Note: Always disconnect the power cord before adding, changing or installing any hardware components.

Before Power On

1. Make sure no short circuits exist between the motherboard and chassis.
2. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse.
3. Remove all add-on cards.
4. Install one CPU (making sure it is fully seated) and connect the chassis speaker and the power LED to the motherboard. (Check all jumper settings as well.)
5. Use only the correct type of CMOS onboard battery as recommended by the Manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

No Power

1. Make sure no short circuits exist between the motherboard and the chassis.
2. Verify that all jumpers are set to their default positions.
3. Check that the 115V/230V switch on the power supply is properly set.
4. Turn the power switch on and off to test the system.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

No Video

1. If the power is on but you have no video, remove all the add-on cards and cables.
2. Use the speaker to determine if any beep codes exist. Refer to the Appendix for details on beep codes.

NOTE

If you are a system integrator, VAR or OEM, a POST diagnostics card is recommended. For I/O port 80h codes, refer to App. B.

Memory Errors

1. Make sure the DIMM modules are properly and fully installed.
2. Determine if different speeds of DIMMs have been installed and verify that the BIOS setup is configured for the fastest speed of RAM used. It is recommended to use the same RAM speed for all DIMMs in the system.
3. Make sure you are using the correct type of ECC DDR2-400 (PC3200) SDRAM (*recommended by the manufacturer.)
4. Check for bad DIMM modules or slots by swapping a single module between two slots and noting the results.
5. Make sure all memory modules are fully seated in their slots. As an interleaved memory scheme is used, you must install two modules at a time, beginning with Bank 0, then Bank 1, and so on (see Section 2-3).
6. Check the position of the 115V/230V switch on the power supply.

Losing the System's Setup Configuration

1. Ensure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information. Refer to Section 1-6 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.

3-2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, note that as a motherboard manufacturer, Super Micro does not sell directly to end-users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please go through the 'Troubleshooting Procedures' and 'Frequently Asked Question' (FAQ) sections in this chapter or see the FAQs on our web site (<http://www.supermicro.com/support/faqs/>) before contacting Technical Support.
2. BIOS upgrades can be downloaded from our web site at (<http://www.supermicro.com/support/bios/>).

Note: Not all BIOS can be flashed depending on the modifications to the boot block code.

3. *If you still cannot resolve the problem, include the following information when contacting Super Micro for technical support:*

- Motherboard model and PCB revision number
- BIOS release date/version (this can be seen on the initial display when your system first boots up)
- System configuration

An example of a Technical Support form is on our web site at (<http://www.supermicro.com/support/contact.cfm>).

4. Distributors: For immediate assistance, please have your account number ready when placing a call to our technical support department. We can be reached by e-mail at support@supermicro.com, by phone at: (408) 503-8000, option 2, or by fax at (408)503-8019.

3-3 Frequently Asked Questions

Question: What are the various types of memory that my motherboard can support?

Answer: The X6DH8-XG2/X6DHE-XG2 has eight 240-pin DIMM slots that support registered ECC DDR2-400 (PC3200) SDRAM modules. It is strongly recommended that you do not mix memory modules of different speeds and sizes.

Question: How do I update my BIOS?

Answer: It is recommended that you **do not** upgrade your BIOS if you are experiencing no problems with your system. Updated BIOS files are located on our web site at <http://www.supermicro.com>. Please check our BIOS warning message and the info on how to update your BIOS on our web site. Also, check the current BIOS revision and make sure it is newer than your BIOS before downloading. Select your motherboard model and download the BIOS file to your computer. Unzip the BIOS update file and you will find the readme.txt (flash instructions), the phlash.exe (BIOS flash utility), the platform.bin (platform file) and the BIOS image (xxxxxx.rom) files. Copy these files onto a bootable floppy and reboot your system. It is not neces-

sary to set BIOS boot block protection jumpers on the motherboard. At the DOS prompt, enter the command "plash." This will start the flash utility and give you an opportunity to save your current BIOS image. Flash the boot block and enter the name of the update BIOS image file.

Question: What's on the CD that came with my motherboard?

Answer: The supplied compact disc has quite a few drivers and programs that will greatly enhance your system. We recommend that you review the CD and install the applications you need. Applications on the CD include chipset drivers for Windows and security and audio drivers.

3-4 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alternation, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

Chapter 4

BIOS

4-1 Introduction

This chapter describes the Phoenix BIOS™ Setup utility for the X6DH8-XG2/X6DHE-XG2. The Phoenix ROM BIOS is stored in a flash chip and can be easily upgraded using a floppy disk-based program.

Note: Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of the Supermicro web site <<http://www.supermicro.com>> for any changes to the BIOS that may not be reflected in this manual.

System BIOS

The BIOS is the Basic Input Output System used in all IBM® PC, XT™, AT®, and PS/2® compatible computers. The Phoenix BIOS stores the system parameters, such as types of disk drives, video displays, etc. in the CMOS. The CMOS memory requires very little electrical power. When the computer is turned off, a backup battery provides power to the CMOS Logic, enabling it to retain system parameters. Each time the computer is powered-on the computer is configured with the values stored in the CMOS Logic by the system BIOS, which gains control at boot-up.

How To Change the Configuration Data

The CMOS information that determines the system parameters may be changed by entering the BIOS Setup utility. This Setup utility can be accessed by pressing the <Delete> key at the appropriate time during system boot. (See below.)

Starting the Setup Utility

Normally, the only visible POST (Power On Self Test) routine is the memory test. As the memory is being tested, press the <Delete> key to enter the main menu of the BIOS Setup utility. From the main menu, you can access the other setup screens, such as the Security and Power menus. Beginning with Section 4-3, detailed descriptions are given for each parameter setting in the Setup utility.



Warning: Do not shut down or reset the system while updating BIOS to prevent possible boot failure.

4-2 Running Setup

**Default settings are in bold text unless otherwise noted.*

The BIOS setup options described in this section are selected by choosing the appropriate text from the main BIOS Setup screen. All displayed text is described in this section, although the screen display is often all you need to understand how to set the options (see next page).

When you first power on the computer, the Phoenix BIOS™ is immediately activated.

While the BIOS is in control, the Setup program can be activated in one of two ways:

1. By pressing <Delete> immediately after turning the system on, or
2. When the message shown below appears briefly at the bottom of the screen during the POST (Power On Self-Test), press the <Delete> key to activate the main Setup menu:

Press the <Delete> key to enter Setup

4-3 Main BIOS Setup

All main Setup options are described in this section. The main BIOS Setup screen is displayed below.

Use the Up/Down arrow keys to move among the different settings in each menu. Use the Left/Right arrow keys to change the options for each setting.

Press the <Esc> key to exit the CMOS Setup Menu. The next section describes in detail how to navigate through the menus.

Items that use submenus are indicated with the ► icon. With the item highlighted, press the <Enter> key to access the submenu.

Main BIOS Setup Menu

PhoenixBIOS Setup - Copyright 1985-2004 Phoenix Technologies Ltd.				
Main	Advanced	Security	Boot	Exit
System Time: [09:26:10] System Date: [11-17-2005] BIOS Date Legacy Diskette A: [1.44/1.25 MB 3½"] Parallel ATA: [Both] Serial ATA: [Enabled] SATA RAID Enable [Disabled] Native Mode Operation: [Auto] ▶ IDE Channel 0 Master ▶ IDE Channel 0 Slave ▶ IDE Channel 1 Master ▶ IDE Channel 1 Slave ▶ IDE Channel 2 Master ▶ IDE Channel 3 Master System Memory: [XXXX KB] Extended Memory: [XXXX KB]		Item Specific Help <Tab>, <Shift-Tab>, or <Enter> selects field.		
F1 Help F4 Select Item -/+ Change Values F9 Setup Defaults Esc Exit ← Select Menu Enter Select ▶ Sub-Menu F10 Previous Values				

Main Setup Features

System Time

To set the system date and time, key in the correct information in the appropriate fields. Then press the <Enter> key to save the data.

System Date

Using the arrow keys, highlight the month, day, and year fields and enter the correct data. Press the <Enter> key to save the data.

BIOS Date

This feature allows the BIOS to automatically display the date when this version of BIOS was built.